(19) World Intellectual Property Organization

International Bureau



(43) International Publication Date 1 September 2005 (01.09.2005)

PCT

(10) International Publication Number WO 2005/081256 A1

(51) International Patent Classification⁷: G11C 11/04

(21) International Application Number:

PCT/SG2005/000022

(22) International Filing Date: 27 January 2005 (27.01.2005)

(25) Filing Language:

inglish

(26) Publication Language:

English

(30) Priority Data:

200400804-1 19 February 2004 (19.02.2004)

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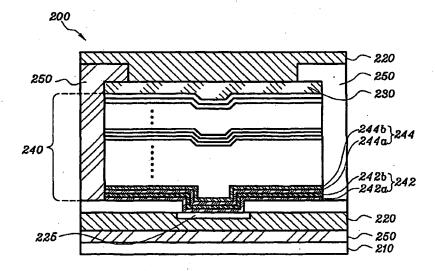
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- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AF, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

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(54) Title: ELECTRICALLY WRITEABLE AND ERASABLE MEMORY MEDIUM



(57) Abstract: A memory cell for an electrically writeable and erasable memory medium as well as a memory medium thereof is provided. The memory cell comprises a data recording element, the data recording element has a plurality of multiple-layer structure disposed one on top of another, each the multiple-layer structure comprising a plurality of sequentially disposed individual layers. At least one of the plurality of individual layers is capable of changing phase between a crystalline state and an amorphous state in response to an electrical pulse, one of the plurality of individual layers having at least one atomic element which is absent from other one of the plurality of individual layers, and the plurality of multiple-layer structure is of a superlattice-like structure to lower a heat diffusion out of the data recording element to shorten a phase change time of the respective individual layers.

WO 2005/081256 A1



(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GII, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TI, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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